

JUN 12 2009

Atty. Dkt. No. 088245-9248

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: LUDWIG, David E.

Title: THREE-DIMENSIONAL IMAGING
PROCESSING MODULE
INCORPORATING STACKED
LAYERS CONTAINING
MICROELECTRONIC CIRCUITS

Appl. No.: 10/806,037

Filing Date: March 22, 2004

Examiner: RATCLIFFE, Luke D.

Art Unit: 3662

Confirmation 1014
Number:

CERTIFICATE OF FACSIMILE TRANSMISSION I hereby certify that this paper is being facsimile transmitted to the United States Patent and Trademark Office, Alexandria, Virginia on the date below. <u>Billie M. Wells</u> (Printed Name) <u>Billie M. Wells</u> (Signature) <u>June 12, 2009</u> (Date of Deposit)

**NOTIFICATION OF LOSS OF ENTITLEMENT TO
SMALL ENTITY STATUS**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

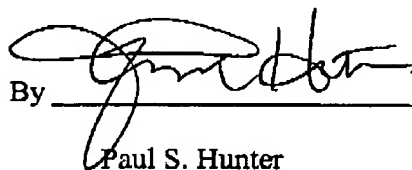
Pursuant to 37 C.F.R. §1.27(g)(2), Applicant hereby states that small entity status in the above-identified application is no longer appropriate. Fees paid presently and henceforth will be paid based on large entity status.

Respectfully submitted,

Date

June 12, 2009

By



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